

**Abdul Manan Sheikh**

Department of ECE,  
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**OBJECTIVE:**

To work for an organization in a competitive and challenging environment, where I can put to use my sound theoretical knowledge and practical insights to optimal usage and create values for my organization as well as realize my ambitions.

**KEY EXPERIENCE:**

I am an Indian citizen aged 42 years, working in the sultanate since 2017 with an overall professional experience of 14 years as an academic and 2 years stint in Industry. I have completed my postgraduate degree (i.e., Master's in Technology) in 2009 with a specialization in Electronics design & Technology from one of the highly ranked central government institutes of India, the National Institute of Technology - Calicut, India. I earned my Bachelor of Engineering degree with a specialization in Electronics & Communication Engineering in the year 2003. My internship at the world's leading FPGAs company Xilinx Inc., USA made me learn and work on cutting-edge EDA tools used in the industry. I experienced real-life projects while working with the R&D team at Xilinx. My interest in FPGA design and HDLs took me to learn and earn 3 months Professional Development Program in VLSI Design from Xilinx authorized training partner.

I started my professional journey as a lecturer in the year 2003 and continued till 2007. I worked in the industry as a digital design engineer between 2009-10 and gained experience in the overseas market through my assignments at Simi Valley, California, USA, and Lancaster - United Kingdom. I worked as an assistant professor in the department of electronics engineering at two highly reputed engineering institutions of India between 2011 to 2017. My tenure as an assistant professor at GDGWI-Lancaster university for almost 5 years helped me to understand UK's higher education system. I was part of the faculty members delivering Lancaster University, Master's in Engineering (MEng.) program in collaboration with GD Goenka World Institute in India which included a faculty exchange program and proved to be effective in honing my skills as an educator.

My employers have commended me for my high level of interpersonal skills and naturally engaging personality. My motivations include learning new things and the challenge of meeting key objectives.

## INTERESTED AREAS:

VLSI, VHDL, Verilog, Electronics Design, Microcontrollers, FPGAs, etc.

## ACADEMIC EXPERIENCE:

**1) Organization : A' Sharqiyah University, Ibra, Sultanate of Oman**

**Designation :** Lecturer

**Tenure :** 7<sup>th</sup> Feb 2022 – Till date

### **Responsibilities**

- (i) Teaching courses with student-centered approach as per the approved course syllabus.
- (ii) Development and Evaluation of various Assessments like Assignments, quizzes, mid-terms, final exams, presentations etc.
- (iii) Developing, conducting, and supervising laboratory experiments for undergraduate students.
- (iv) Advising students concerning their study plan, counseling students regularly concerning their academic performances, referring to student affairs department for appropriate support.
- (v) Developing course materials such as handouts, practice exercises, short videos, lecture videos, online lecturing in MS teams/Zoom platform, developing course pages in LMS (Moodle), Industry site visits, usage of simulation tools and approaches that can enhance the students learning and understanding process.
- (vi) Assist the line managers (HOD/Dean) in the administrative tasks such as course offerings, new lab and program development exercises, solutions and suggestions proposals to the existing problems, and any other task assigned by them.

**Subjects Taught:** Electronics I, II, Circuits I, Analog Communications, Optical Communications, Microprocessors & Microcontrollers, Power Electronics, Communication Electronics, Digital Communications, Wireless Communications.

**2) Organization : A' Sharqiyah University, Ibra, Sultanate of Oman**

**Designation :** Laboratory Technician

**Tenure :** 19<sup>th</sup> March 2017 – 6<sup>th</sup> Feb 2022

### **Responsibilities**

- (i) Supervising students during lab sessions including delivery of theoretical background of experiments conducted, demo, and debugging exercises, in addition to coordinating with faculty members in delivering laboratory experiments.
- (ii) Laboratory setup and implementation, equipment selection & purchasing, assembling of experimental circuitry, technical manuals.

- (iii) Assisting in laboratory management including software installation like Matlab, MultiSim, Altera Quartus design suite, ModelSim, Arduino IDE, C Compiler, Fusion splicer 19/70S fiber core alignment fusion splicer, etc.
- (iv) Maintenance/repair of lab equipment, physical inventory maintenance, and tracking, lab policies, and procurement of lab consumables and assets supplies.
- (v) Failure analysis support for contract manufacturers as well as for field organizations.

3) **Organization** : **G D G World Institute – Gurgaon & Lancaster University-UK**  
**Designation** : Asst. Professor, Department of Electrical & Electronics  
**Tenure** : 4/7/2012– 15/03/2017  
**Subjects Taught** : Electronic circuits, Digital design, VHDL, Advanced Embedded Systems. Supervised Academic projects and three projects were presented at university-level conferences.

(All theoretical, practical modules and exams are moderated at Lancaster University - UK and externally moderated at various universities of UK/USA.)

***Additional administrative responsibilities***

- (i) ***Academic Conduct officer:*** Responsible for the investigation of and subsequent action for plagiarism in coursework at the undergraduate level.
- (ii) ***Projects Coordinator:*** Responsible for coordinating with final and pre-final year projects like evaluations, achievable, reporting, etc.
- (iii) ***GDGWI-Mitsubishi Partnership:*** Responsible for executing MOU and running a collaboration for spreading awareness about automation amongst students for 2+ years.
- (iv) ***Active Member of Placement Steering committee and Admissions marketing team.***

4) **Organization** : **Ajay Kumar Garg Engineering College, Ghaziabad**  
**Designation** : Asst. Professor, Department of Electronics & Communication  
**Tenure** : 1/1/2011– 30/6/2012  
**Subjects Taught** : Electronic circuits, Microprocessors, Microcontrollers, etc.

5) **Organization** : **P.A. College of Engineering, Mangalore (Karnataka)**  
**Designation** : Lecturer to Asst. Prof., Department of Telecommunication.  
**Tenure** : September 2003 – October 2007.  
**Subjects Taught** : Satellite Communication, Microprocessors 8085, 8086, Electronic circuits, Computer communication networks, VHDL etc.

## **Industry/Research Experience**

**1) Organization** : **Enventure Technologies**, Bangalore & **Innovative Integration**, United States

**Designation** : Design Engineer (Oct 2009 – Dec 2010)

**Project** : Worked as a team member for the development of products covering the full spectrum of real-time systems: PC-based real-time data acquisition, DSP-based signal processing, and hardware-assisted (FPGA) signal processing.

**2) Organization** : **Xilinx India Technology Services Pvt. Ltd., Hyderabad**

**Designation** : Intern (April 2008 – Aug 2008)

**Project** : Understanding the SVA and Development of monitor based on SVA for Xilinx specific protocol that can be reused.

## **EDUCATIONAL QUALIFICATIONS:**

- **Master of Technology (M.Tech)** with a specialization in Electronics Design and Technology at a public institution i.e., **National Institute of Technology (NIT) – Calicut** in May 2009.
- **Bachelor of Engineering (B.E.)** degree with First class distinction marks in Electronics & Communication (E&C) branch in July 2003 from **Vishweshwariah Technological University**, Belgaum, Karnataka (India).
- Professional Development Certificate in VLSI design from Sandeepani school of VLSI, Coreel Technologies, Bangalore in 2009.
- Matriculation and (10+2) from Kendriya Vidyalaya, **C.B.S.E** Board, Air force, Gorakhpur (U.P) in 1998.

## **LANGUAGE PROFICIENCY:**

English, Hindi, Urdu

## **COMPUTER PROFICIENCY:**

**Languages:** C, VHDL, Verilog, Arduino IDE  
**Office-suite:** MS-Office, Word, Open-office.  
**Packages:** VLSI Design, Microprocessor 8085, 8086 & 8051 Microcontroller, Motorola Microcontroller and Embedded Controllers (PIC), Orcad-Layout plus, Microwind(VLSI Layout design), Matlab, MultiSim, Altera Quartus design suite, ModelSim, Arduino IDE, HSpice, Fusion splicer 19/70S fiber core alignment fusion splicer.

## PUBLICATION:

- Mahmoud A. M. Albreem, **Abdul Manan Sheikh** and Ayman El Saleh, "Towards A Sustainable Environment with A Green IoT: An Overview" 2022 International Conference on Computer Technologies co-organized by Universiti Malaysia Pahang (UMP), Universiti Malaysia Perlis (UniMAP), FH JOANNEUM University of Applied Sciences (Austria) is accepted for the publication.
- Honnurvali Mohamed Shaik, Adnan Kabbani, **Abdul Manan Sheikh**, Keng Goh, Naren Gupta, Tariq Umar. Measurement and validation of polysilicon photovoltaic module degradation rates over five years of field exposure in Oman[J]. AIMS Energy, 2021, 9(6): 1192-1212. doi: 10.3934/energy.2021055
- M. A. Albreem, **A. M. Sheikh**, M. H. Alsharif, M. Jusoh, and M. N. Mohd Yasin, "Green Internet of Things (GIoT): Applications, Practices, Awareness, and Challenges," in IEEE Access, vol. 9, pp. 38833-38858, 2021, doi: 10.1109/ACCESS.2021.3061697.
- **A. Manan**, "Efficient 16 nm SRAM Design for FPGA's," 2018 5th International Conference on Signal Processing and Integrated Networks (SPIN), Noida, India, 2018, pp. 457-461, doi: 10.1109/SPIN.2018.8474069.
- **A. Manan**, "Pre Bond-Through Silicon Vias (TSVs) Testing in 3D ICs" in AKGEC International Journal of Technology, Vol 7, No. 1, ISSN 0975-9514
- A. K. Singh, **A. Manan**, "Design and performance analysis of constant  $g_m$  low power rail-to-rail operational amplifier" in International Journal of Recent Technology and Engineering, Vol 1, Issue 6 January 30, 2012.
- **A. Manan**, "Implementation of image processing algorithm on FPGA", National Conference on Emerging trends in computing and communication ETCC-2008, NIT Hamirpur.

## PROFESSIONAL MEMBERSHIPS :

- Member IEEE
- Life Member Indian Society Technical Education
- HeartSaver First Aid CPR AED, American Heart Association (4baef0ec43c2)

## PERSONAL PROFILE:

**Date of Birth** : 24- 09-1979  
**Sex** : Male  
**Nationality** : Indian  
**Passport Number** : L8757089  
**Religion** : Islam  
**Father's Name** : Abdul Gafoor  
**Permanent Address** : H.no-42 Q, Near Ashoka Gas Godown, Singharia,  
P. O. - Kunraghat, Gorakhpur (U.P.) - 273008

## STRENGTHS:

Punctual, hardworking dedicated and holds a positive attitude to work and get along with people in society, reliable, trustworthy, and straightforward person with a friendly disposition and enable others to reach a mutually acceptable decision on the matter of common interest.

## HOBBIES AND INTERESTS:

- Reading
- Playing cricket
- Interacting with people
- Organizing events

## REFERENCES:

Available on request.

## DECLARATION:

I hereby declare that the above information furnished by me is correct and actual to the best of my knowledge.

  
ABDUL MANAN

**Date:** December 2023

**Place:** Oman

**Signature:** \_\_\_\_\_